

Notice of References Cited

Application/Control No.

09/847,138

Applicant(s)/Patent Under
Reexamination
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Examiner

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Art Unit

2123

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,647,365	07-1997	Abboud, William J.	600/447
*	B	US-5,905,692	05-1999	Dolazza et al.	367/123
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*	D	US-5,322,068	06-1994	Thiele et al.	600/447
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"HDL Chip Design" by Douglas J. Smith, Doone Publications, 1996, ISBN 0-9651934-3-8, pages 2-19.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.